

prior to enabling a central processing unit to access the digital video, determine whether the data access parameter restricts accessing of the digital video; and

when the data access parameter restricts accessing of the digital video, prevent the central processing unit from accessing the digital video without restriction.

12. (New) The method of claim 11 wherein the incoming data includes an embedded data access parameter and wherein the method includes storing the digital video in memory wherein the stored digital video in the memory does not include the embedded data access parameter.

13. (New) The method of claim 11 wherein preventing the central processing unit from accessing the digital video without restriction includes controlling access to the digital video to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video.

14. (New) The method of claim 13 wherein providing at least one of: copy restriction, viewing restriction and use restriction of the digital video includes controlling access to the digital video to provide a viewing option, parental control, still frame copy restriction, copying with copyright notices, and reduced quality copying.

15. (New) A computing system to provide protection of incoming data that includes an embedded data access parameter comprising:

a video digitizer circuit operative to receive data of at least one of analog audio data and analog video data, wherein a line of the data includes screen end information, a data access parameter, color burst information, and at least one of audio and video data and wherein the data access parameter is independent of a source of the data and digitize, independent of the data access parameter, at least one of audio and video data to produce digital video, wherein once the at least one of audio and video data is digitized, the data access parameter is lost;

memory operatively coupled to the video decoder, for storing the digital video wherein the stored video data does not include the data access parameter;

a protection detection circuit operative to detect the presence of the embedded data access parameter and provide an indication of protection based on the embedded data access parameter when the embedded data access parameter is detected wherein the indication of protection indicates one of a plurality of different types of data access; and

at least one of: a central processing unit and another computer element, responsive to the indication of protection from the protection detection circuit and operative to process the stored video data based on the indication of protection.

16. (New) A method for a computer system to protect access to video data received from an analog video signal that includes an embedded data access parameter comprising:

receiving an indication of data access restriction for stored digital video data that is stored in memory, based on the embedded data access parameter, wherein the stored digital video data does not include the embedded data access parameter from the analog video signal; and

processing the stored digital video data in accordance with the received indication of data access restriction.

17. (New) The method of claim 16 wherein the indication of data access restriction indicates one of a plurality of different types of data access of the stored digital video data and wherein processing the stored digital video data in accordance with the received indication of data access restriction includes controlling access to the stored digital video data to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video data.

18. (New) The method of claim 17 wherein controlling to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video includes controlling access to the stored digital video data to provide at least one of a viewing option, parental control, still frame copy restriction, copying with copyright notices, and reduced quality copying.

19. (New) A computer system to protect access to video data received from an analog video signal that includes an embedded data access parameter comprising:

memory containing stored digital video data obtained from the analog video signal wherein the stored digital video data does not include the embedded data access parameter from the analog video signal; and

at least one of: a central processing unit, a computer element and a peripheral device, operatively coupled to the memory, and operative to receive an indication of data access restriction for stored digital video data that is stored in memory, based on the embedded data access parameter, and operative to process the stored digital video data in accordance with the received indication of data access restriction.

20. (New) The computer system of claim 19 wherein the indication of data access restriction indicates one of a plurality of different types of data access of the stored digital video data and wherein the at least one of the CPU, computer element and peripheral device processes the stored digital video data in accordance with the received indication of data access restriction and includes controlling access to the stored digital video data to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video data.

21. (New) The computer system of claim 20 wherein the at least one of the CPU, computer element and peripheral device provides at least one of: copy restriction, viewing restriction and use restriction of the digital video by controlling access to the digital video data to provide at least one of a viewing option, parental control, still frame copy restriction, copying with copyright notices, and reduced quality copying.